



2681-As
12-06-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: TOD PAULUS ET AL.

Filed: FEBRUARY 12, 2002

For: NOTCH FILTER FOR DC OFFSET REDUCTION IN RADIO-FREQUENCY APPARATUS AND ASSOCIATED METHODS

Serial No.: 10/079,099

Group Art Unit: 2681

Examiner: UNKNOWN

Atty Dkt: SILA:097

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Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in a first class, postage prepaid envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on the date below:

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Date

Marty Bowler
Name

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98, it is respectfully requested that this Information Disclosure Statement be entered and the document(s) listed on attached Form PTO-1449 be considered by the Examiner and made of record.

In accordance with 37 C.F.R §§ 1.97(g),(h), this Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

The present Information Disclosure Statement is being filed prior to the receipt of a first Official Action reflecting an examination on the merits, and hence is believed to be timely filed in

accordance with 37 C.F.R. § 1.97(b). No fees are believed to be due in connection with the filing of this Information Disclosure Statement, however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to these materials, the Commissioner is hereby authorized to deduct said fees from Deposit Account No. 10-1205/SILA:097.

Per 37 CFR 1.98(d), no copies of references A1-A48, B1-B6 and C1-91 have been provided, as copies of these references have been previously submitted to the Office in one or more of co-pending U.S. Patent Application Serial Nos. 09/821,340 filed on March 29, 2001, which is entitled "Digital Interface In Radio-Frequency Apparatus And Associated Methods" and 09/821,342 filed on March 29, 2001, which is entitled "Partitioned Radio-Frequency Apparatus And Associated Methods" and which is relied upon by the present application for an earlier effective filing date under 35 U.S.C. Section 120.

Applicant respectfully requests that the listed document(s) be made of record in the present case.

Respectfully submitted,



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Enclosures

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List of Patents and Publications for Applicant's

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	A1	5,828,955	10/27/98	Lipowski et al.			8/30/95
	A2	6,035,186	3/7/00	Moore et al.			3/11/97
	A3	6,075,979	6/13/00	Holtvoeth et al.			3/5/97
	A4	5,764,171	6/9/98	Stikvoort			4/2/96
	A5	6,148,048	11/14/00	Kerth et al.			9/26/97
	A6	4,713,563	12/15/87	Marshall et al.			5/12/86
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	A22	5,451,948	9/19/95	Jekel			2/28/94
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	A24	5,557,642	9/17/96	Williams			11/14/94
	A25	5,712,628	1/27/98	Phillips et al.			8/31/95
	A26	5,742,189	4/21/98	Yoshida et al.			9/14/95
	A27	5,862,465	1/19/99	Ou			12/30/96
	A28	5,973,601	10/26/99	Campana			12/2/97
	A29	5,758,276	5/26/98	Shirakawa et al.			5/31/96
	A30	5,740,524	4/14/98	Pace et al.			12/14/95
	A31	4,623,926	11/18/86	Sakamoto			11/9/836
	A32	5,341,135	8/23/94	Pearce			4/30/92
	A33	5,241,310	8/31/93	Tiemann			3/2/92
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	A36	5,469,475	11/21/95	Voorman			5/31/91
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	A39	4,692,737	9/8/87	Stikvoort et al.			10/17/86
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	A41	4,797,845	1/10/89	Stikvoort			12/11/86
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Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
	A47	6,323,735	11/27/01	Welland et al.			5/25/00
	A48	6,167,245	12/26/00	Welland			5/29/98

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	B1	WO 00/22735	4/20/00	Ali			
	B2	GB2233518A	1/9/91	Dedic			
	B3	0643477A2	3/15/95	Hulkko et al.			
	B4	WO 00/11794	3/2/00	Moore et al.			
	B5	WO 00/01074	1/6/00	Van Der Zwan et al.			
	B6	WO 99/22456	5/6/99	Grenabo			10/27/98

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	C1	Stephen Jantzi et al., "Quadrature Bandpass $\Delta\Sigma$ Modulation for Digital Radio," IEEE Journal of Solid-State Circuits, Vol. 32, No. 12, December 1997, pp. 1935-1950.
	C2	Stephen Jantzi et al., "A Complex Bandpass $\Delta\Sigma$ Converter For Digital Radio," ISCAS, May/June 1994, pp. 453-456.
	C3	"Analog Devices Delivers World's First Open Market GSM Direct Conversion Radio Chipset," Analog Devices Corporate Information Press Release, http://contentanalog.com/pressrelease/prdisplay/0,1622,102,00.html , September 13, 1999, pp. 1-4.

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	C6	Jan Crols et al., "Low-IF Topologies for High-Performance Analog Front Ends of Fully Integrated Receivers," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 45, No. 3, March 1998, pp. 269-282.	
	C7	Jacques C. Rudell et al., "Recent Developments In High Integration Multi-Standard CMOS Transceiver for Personal Communication Systems," invited paper at the 1998 International Symposium on Low Power Electronics, Monterey, California, 6 pgs.	
	C8	Asad Abidi, "CMOS Wireless Transceivers: The New Wave," IEEE Communications Magazine, August 1999, pp. 119-124.	
	C9	Data Sheet, UAA3535HL, "Low Power GSM/DCS/PCS Multi-band Transceiver," Philips Semiconductors, February 17, 2000, pp. 1-24.	
	C10	Stephen Jantzi et al., "FP 13.5: A Quadrature Bandpass $\Delta\Sigma$ Modulator for Digital Radio," Digest of Technical Papers, 1997 IEEE International Solid-State Circuits Conference, First Edition, February 1997, pp. 216-217, 460.	
	C11	S. A. Jantzi et al., "The Effects of Mismatch In Complex Bandpass $\Delta\Sigma$ Modulators," IEEE, 1996, pp. 227-230.	
	C12	Qiuting Huang, "CMOS RF Design-The Low Power Dimension," IEEE 2000 Custom Integrated Circuits Conference, pp. 161-166.	
	C13	Paolo Orsatti et al., "A 20-mA-Receive, 55-mA-Transmit, Single-Chip GSM Transceiver in 0.25- μ m CMOS," IEEE Journal of Solid-State Circuits, Vol. 34, No. 12, December 1999, pp. 1869-1880.	
	C14	Qiuting Huang et al., "The Impact of Scaling Down to Deep Submicron on CMOS RF Circuits," IEEE Journal of Solid-State Circuits, Vol. 33, No. 7, July 1998, pp. 1023-1036.	
	C15	Behzad Razavi, "Design Considerations for Direct-Conversion Receivers," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 44, No. 6, June 1997, pp. 428-435.	

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	C16	Farbod Behbahani et al., "CMOS Mixers and Polyphase Filters for Large Image Rejection," IEEE Journal of Solid-State Circuits, Vol. 36, No. 6, June 2001, pp. 873-887.
	C17	Jan Crols et al., "A Single-Chip 900 MHz CMOS Receiver Front-End With A High Performance Low-IF Topolgy," IEEE Journal of Solid-State Circuits, Vol. 30, No. 12, December 1995, pp. 1483-1492.
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	C19	Analog Devices, AD6523/AD6524, GSM Direct Conversion Radio Chip Set, www.analog.com , 2 pgs.
	C20	Analog Devices, GSM 3 V Transceiver IF Subsystem, AD6432, www.analog.com , pp. 1-20.
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	C23	Analog Devices, AD20msp415, GSM/DCS1800/PCS1900, Baseband Processing Chipset, Rev. O, 1997, pp. 1-7.
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	C26	Atkinson et al., "A Novel Approach To Direct Conversion RF Receivers For TDMA Applications," Analog Devices, 1999, pp. 1-5.
	C27	Crochiere et al., "Optimum FIR Digital Filter Implementations For Decimation, Interpolation, And Narrow-Band Filtering," IEEE Transactions On Acoustics, Speech, And Signal Processing, Vol. ASSP-23, No. 5, October 1975, pp. 444-456.
	C28	Hogenauer, "An Economical Class Of Digital Filters For Decimation And Interpolation," IEEE, 1981, pp. 155-162.
	C29	Brandt et al., "A Low-Power, Area-Efficient Digital Filter For Decimation And Interpolation," IEEE Journal Of Solid-State Circuits, Vol. 29, No. 6, June 1994, pp. 679-687.

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	C31	D'Avella et al., "An Adaptive MLSE Receiver For TDMA Digital Mobile Radio," IEEE Journal On Selected Areas In Communications," Vol. 7, No.1, January 1989, pp. 122-129.
	C32	Razavi, "CMOS RF Receiver Design For Wireless LAN Applications," IEEE, 1999, pp. 275-280.
	C33	Lucent Technologies, "W3020 GSM Multiband RF Transceiver," Advance Data Sheet, December 1999, pp. 1-44.
	C34	Lucent Technologies, "DSP1620 Digital Signal Processor," Data Sheet, June 1998, pp. 1-178.
	C35	Steyaert et al., "A 2-V CMOS Cellular Transceiver Front-End," IEEE Journal of Solid-State Circuits, Vol. 35, No. 12, December 2000, pp. 1895-1907.
	C36	Paulus et al., "A CMOS IF Transceiver With Reduced Analog Complexity," IEEE Journal Of Solid-State Circuits, Vol. 33, No. 12, December 1998, pp. 2154-2159.
	C37	Analog Devices, "Analog Devices Delivers World's First Open Market GSM Direct Conversion Radio Chipset," November 1999, 4 pgs.
	C38	"Digest Of Technical Papers," 1997 IEEE International Solid-State Circuits Conference, First Edition, February 1997, 5 pgs.
	C39	RF Micro Devices, RF2968, Product Description, Blue Tooth Transceiver, Rev A19, pp. 11-199-11-222.
	C40	Texas Instruments, TRF6901, "Single Chip RF Transceiver," March 2002, pp. 1-29.
	C41	Texas Instruments, TRF6900A, "Single Chip RF Transceiver," September 2001, pp. 1-34.
	C42	Texas Instruments, TRF6900, "Single Chip RF Transceiver, October 1999, pp. 1-32.
	C43	Philips Semiconductor, "Bluetooth RF Transceiver," Data Sheet, UAA3558, December 21, 2000, pp. 1-5.
	C44	Philips Semiconductor, "Image Reject 1 800 MHz Transceiver For DECT Applications," Data Sheet, UAA2067G, October 22, 1996, pp. 1-24.

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	C46	Philips Semiconductor, "900 MHz Analog Cordless Telephone IC," Data Sheet, UAA3515A, December 12, 2001, pp. 1-44.
	C47	Philips Semiconductor, "Low Voltage IF I/Q Transceiver," Data Sheet, SA1638, September 3, 1997, pp. 1-26.
	C48	Texas Instruments, "TCS2100 GPRS Chipset Solution," Product Bulletin, 2001, 4 pgs.
	C49	Fague, "Othello: A New Direct-Conversion Radio Chip Set Eliminates IF Stages," Analog Dialogue 33-10, 1999, pp. 1-3.
	C50	Analog Devices, AD6523/AD6524, "GSM Direct Conversion Radio Chip Set," 1999, 2 pgs.
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	C53	Lucent Technologies, "Trident," Product Brief, February 2001, 2 pgs.
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	C55	Micro Linear, "ML2712 2.4GHz Transceiver," Datasheet, August 2001, pp. 1-21.
	C56	Analog Devices, "GSM/GPRS/DCS1800.PCS1900 SoftFone Baseband Chipset," AD20msp430, 2000, 2 pgs.
	C57	RF Micro Devices, "Polaris Total Radio Solution," Press Release, 2002, 1 pg.
	C58	Tuttle, "Introduction To Wireless Receiver Design," Tutorial, 2002, pp. 2-58.
	C59	Rael et al., "Design Methodology Used In A Single-Chip CMOS 900 MHz Spread-Spectrum Wireless Transceiver," 35 th Design Automation Conference, June 1998, 6 pgs.
	C60	Troster et al., "An Interpolative Bandpass Converter On A 1.2- μ m BiCMOS Analog/Digital Array," IEEE Journal Of Solid-State Circuits, Vol. 28, No. 4, April 1993, pp. 471-477.

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	C61	Schreier et al., "Decimation For Bandpass Sigma-Delta Analog-To-Digital Conversion," IEEE, 1990, pp. 1801-1804.
	C62	Shoaei et al., "Optimal (Bandpass) Continuous-Time $\Delta\Sigma$ Modulator," pp. 489-492.
	C63	Schreier et al., "Bandpass Sigma-Delta Modulation," Electronics Letters, Vol. 25, no. 23, November 9, 1989, pp. 1560-1561.
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	C65	Crols et al., "An Analog Integrated Polyphase Filter For A High Performance Low-IF Receiver," Symposium On VLSI Circuits Digest Of Technical Papers, 1995, pp. 87-88.
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	C67	Jantzi, "A Fourth-Order Bandpass Sigma-Delta Modulator," IEEE Journal Of Solid-State Circuits, Vol. 28, No. 3, March 1993, pp. 282-291.
	C68	Liu et al., "Switched-Capacitor Implementation Of Complex Filters," IEEE International Symposium On Circuits And Systems, Vol. 3, 1986, 5 pgs.
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	C70	Thurston et al., "Bandpass Implementation Of The Sigma-Delta A-D Conversion Technique," International Conference On Analogue To Digital And Digital To Analogue Conversion, September 1991, 7 pgs.
	C71	Rudell, et al., "Second Generation Multi-Standard Monolithic CMOS RF Transceiver," University of California, Berkeley, Slides 1 through 9 (June 1996)
	C72	Cho, et al., "Multi-Standard Monolithic CMOS RF Transceiver," University of California, Berkeley, Slides 1 through 26 (June 1996)
	C73	Copending U.S. Patent Application Serial No. 09/821,342, filed March 29, 2001, "Partitioned Radio-Frequency Apparatus And Associated Method" (SILA:072)
	C74	Copending U.S. Patent Application Serial No. 09/821,340, filed March 29, 2001, "Digital Interface In Radio-Frequency Apparatus And Associated Methods" (SILA:073)

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	C75	Copending U.S. Patent Application Serial No. 10/075,094, filed February 13, 2002, "Radio-Frequency Communication Apparatus And Associated Methods" (Sila:074)
	C76	Copending U.S. Patent Application Serial No. 10/075,098, filed February 13, 2002, "Apparatus And Methods For Generating Radio Frequencies In Communication Circuitry" (Sila:075)
	C77	Copending U.S. Patent Application Serial No. 10/075,122, filed February 12, 2002, "Digital Architecture For Radio-Frequency Apparatus And Associated Methods" (Sila:078)
	C78	Copending U.S. Patent Application Serial No. 10/083,633, filed February 26, 2002, "Apparatus And Methods For Calibrating Signal-Processing Circuitry" (Sila:080)
	C79	Copending U.S. Patent Application Serial No. 10/081,121, filed February 22, 2002, "Calibrated Low-Noise Current And Voltage References And Associated Methods" (Sila:095)
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Form PTO-1449 (modified)Atty. Docket No.
SILA:097Serial No.
10/075,099

List of Patents and Publications for Applicant's

Applicants
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Filing Date:
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2681**DEC 04 2002**U.S. Patent Documents
See Pages 1-3Foreign Patent Documents
See Pages 3Other Art
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